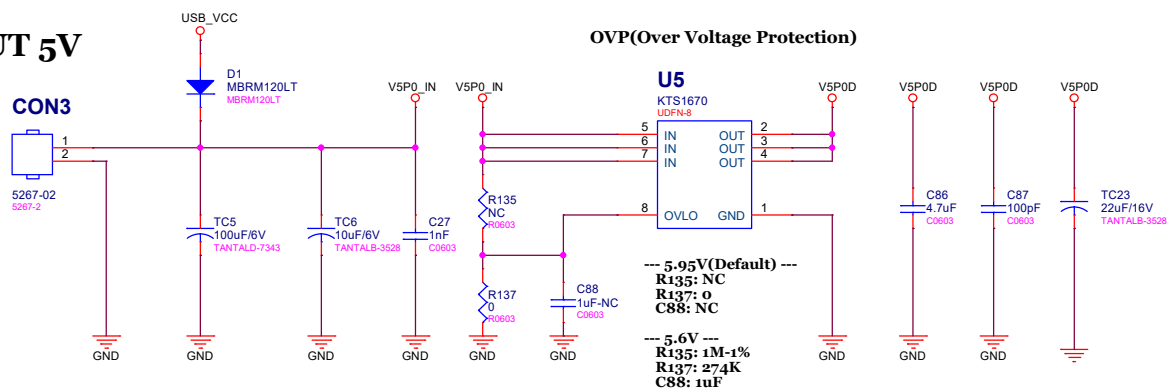
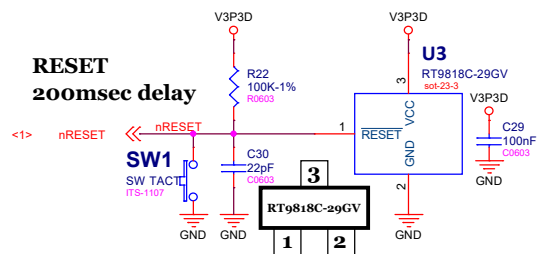


INPUT 5V

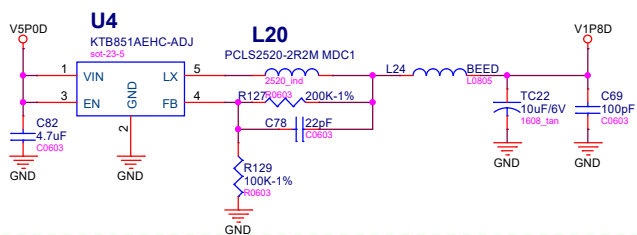
OVP(Over Voltage Protection)



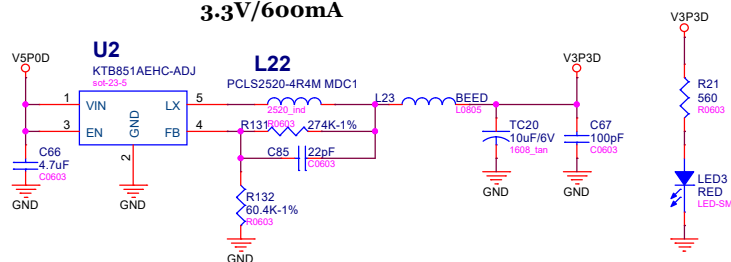
RESET 200msec delay



외부 LDO 사용시 R23 제거후 부품 장착 1.8V/600mA



3.3V/600mA



adStar-D TRIP

Version	Date	Description
Ver : 1.0	Feb 13, 2012	First release version
Ver : 1.1	Feb 21, 2012	외부 LDO 회로 추가 (R23, TC4, TC9, C30, C31, U4)
Ver : 2.0	July 13, 2018	***** 필수 수정 사항 ***** NAND Boot Mode: only 4bit ECC Mode Reset IC add ("RT9818C-29GV")
		1. OVP(Over Voltage Protection) add ("KTS1670") 2. 1.8V, 3.3V LDO Change (LM1118 -> "KTB851AEHC-ADJ") 3. LCD Backlight Change(R1204N313D-FE->"KTD2511DEHD-TR")
Ver : 2.1	Nov 19, 2018	JP1 14Pin->10Pin으로 변경