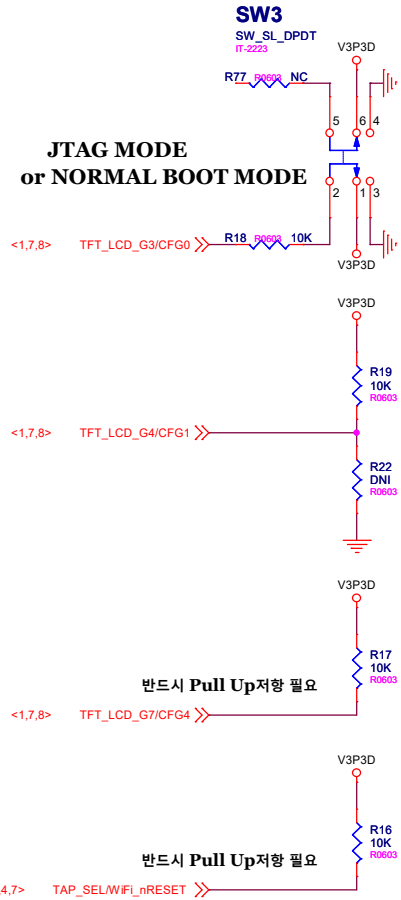
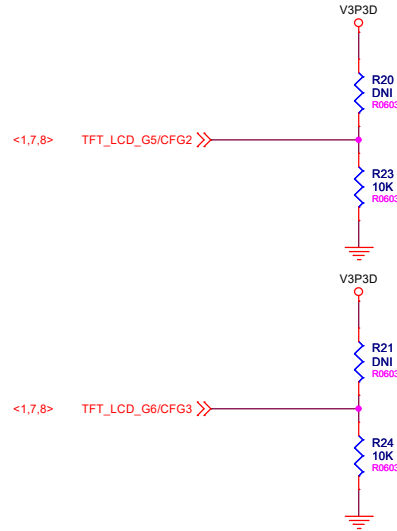


## JTAG MODE or NORMAL BOOT MODE



## Boot Selection



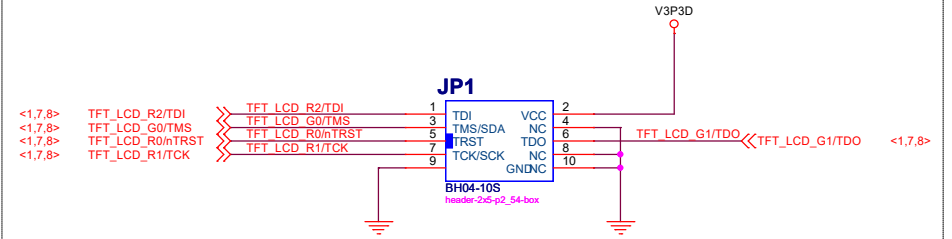
## Boot Mode Selection

BOOT MODE	CFG[0..4]				
	0	1	2	3	4
JTAG DEBUG MODE	L	X	X	X	H
Serial Flash Boot Mode	H	H	L	L	H
NAND Boot Mode (4bit ECC)	H	L	H	L	H

## User Mode Selection

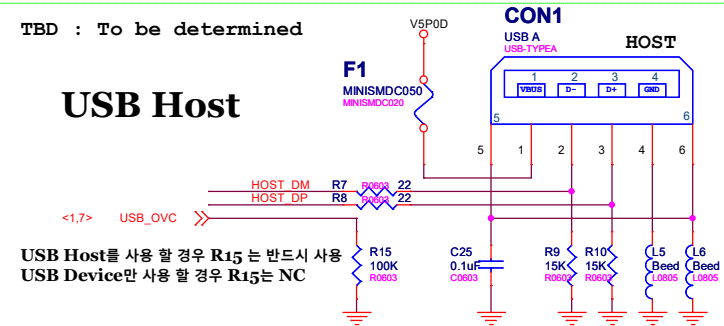
USER MODE	SW4	SW5
Execute	H	H
Mass Storage(NAND)	H	L
Mass Storage(SD Card)	L	H
USB Communication	L	L

## JTAG Connector

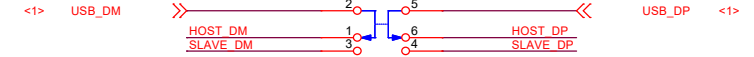


TBD : To be determined

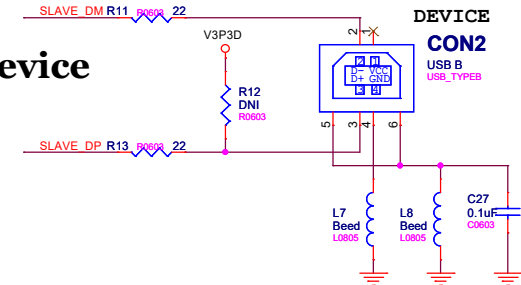
## USB Host

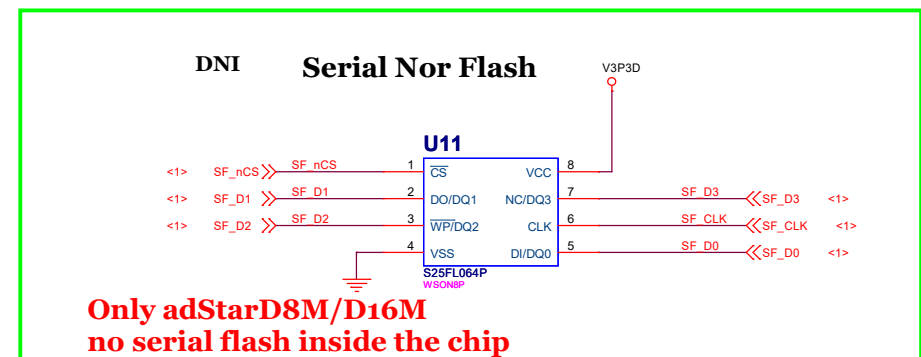
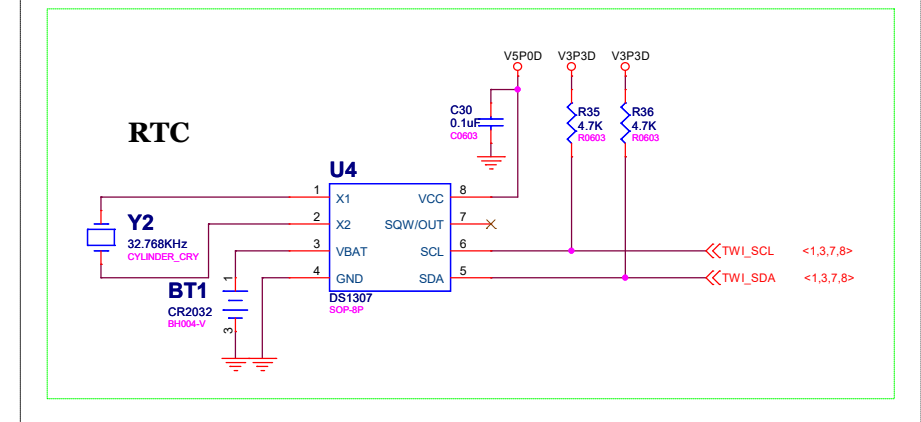
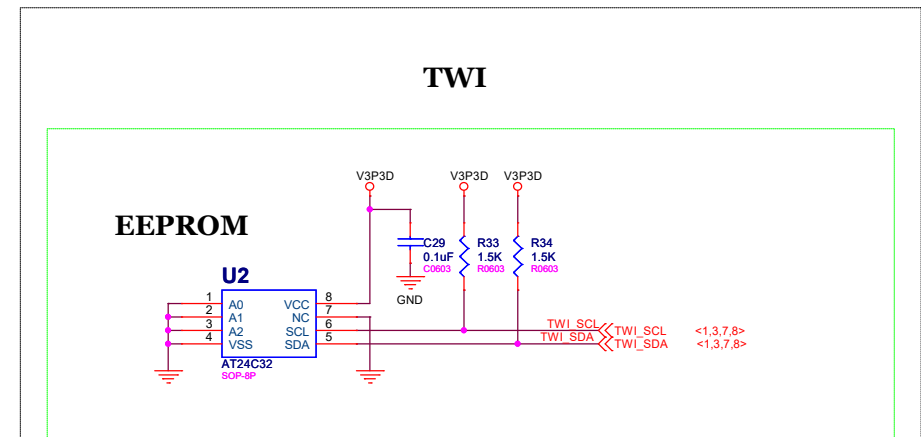
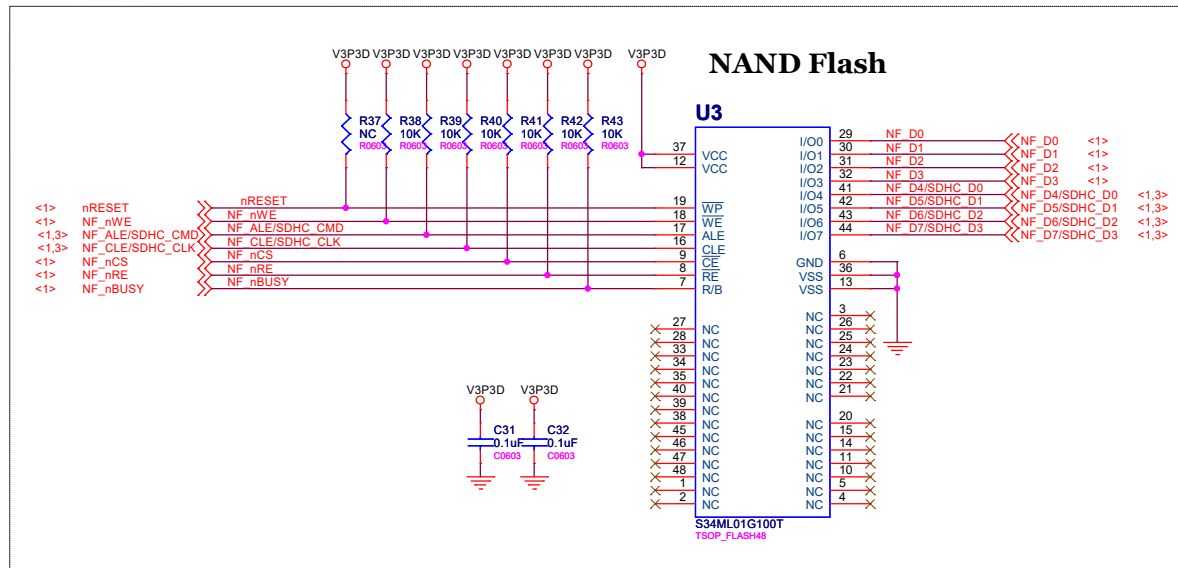
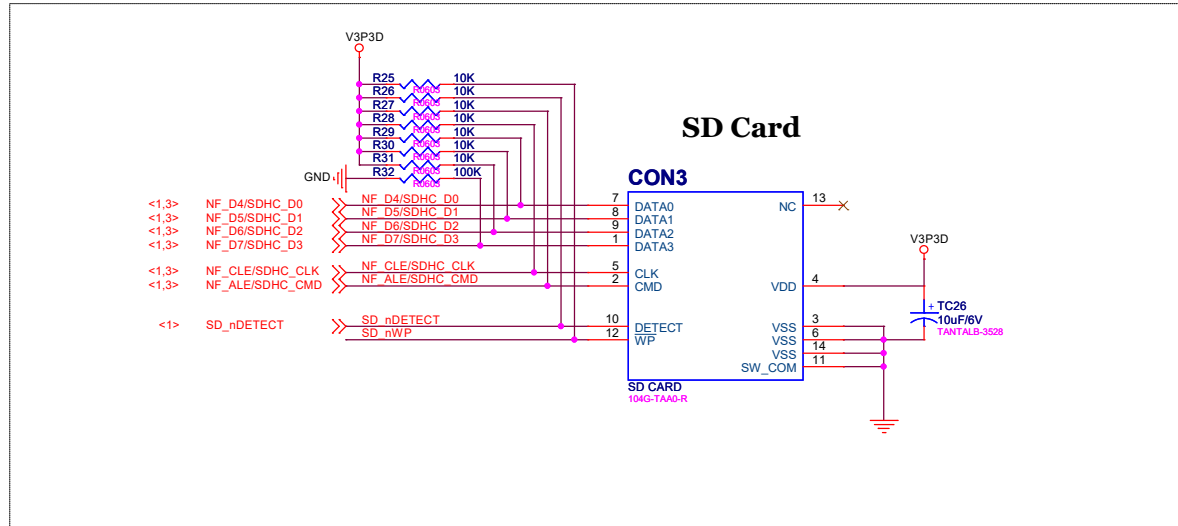


## USB Device, Host Select Switch

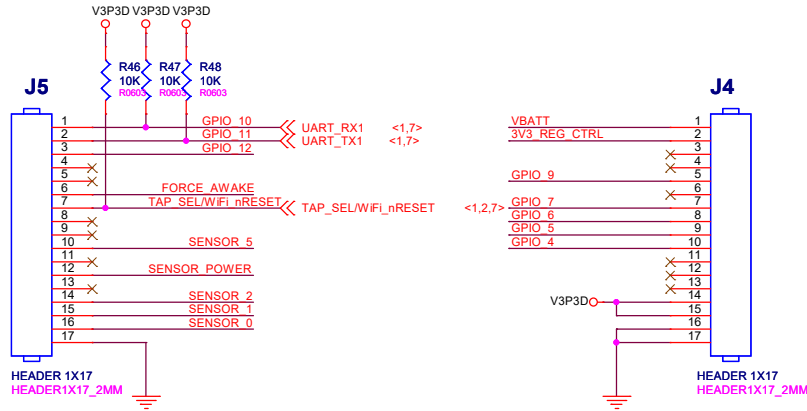


## USB Device

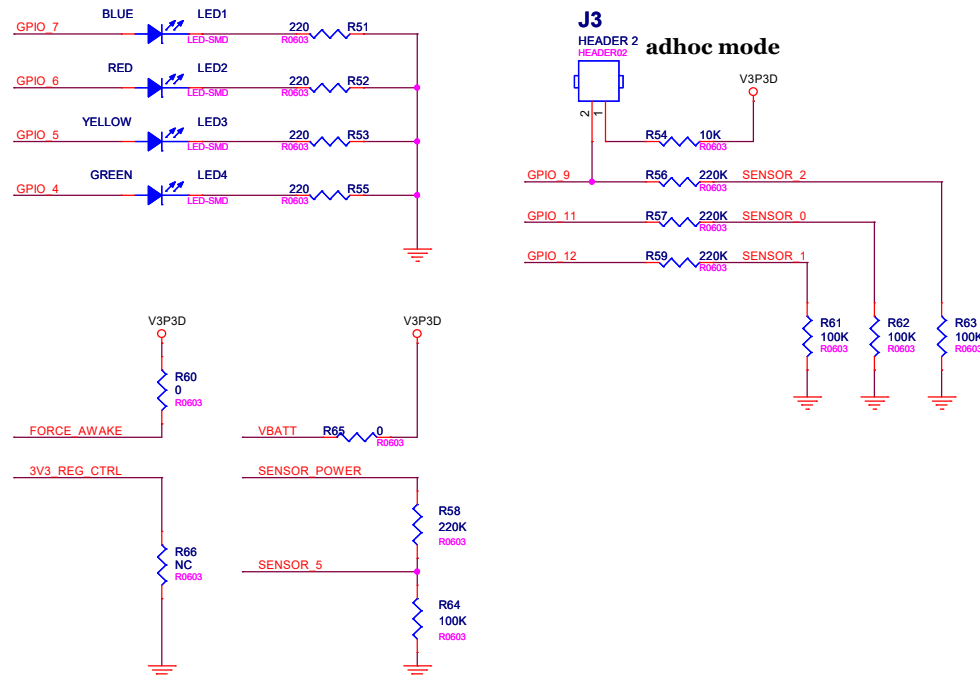




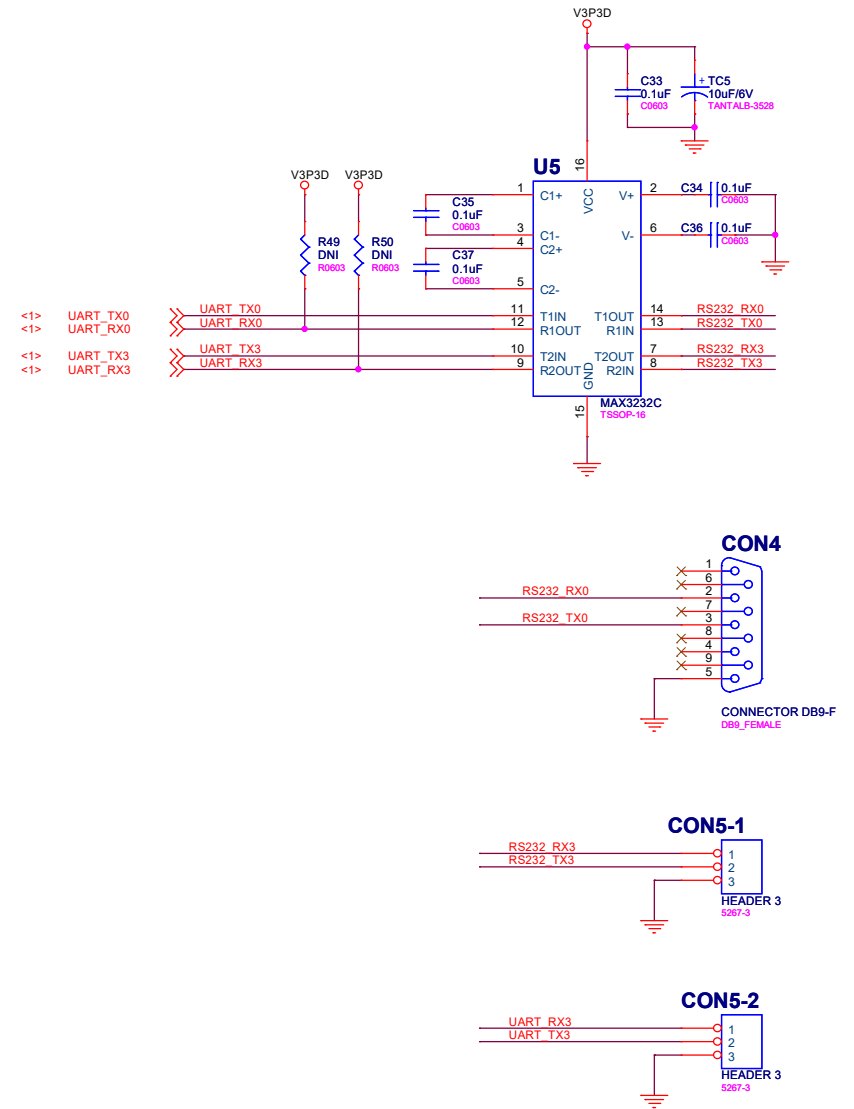
## WiFi



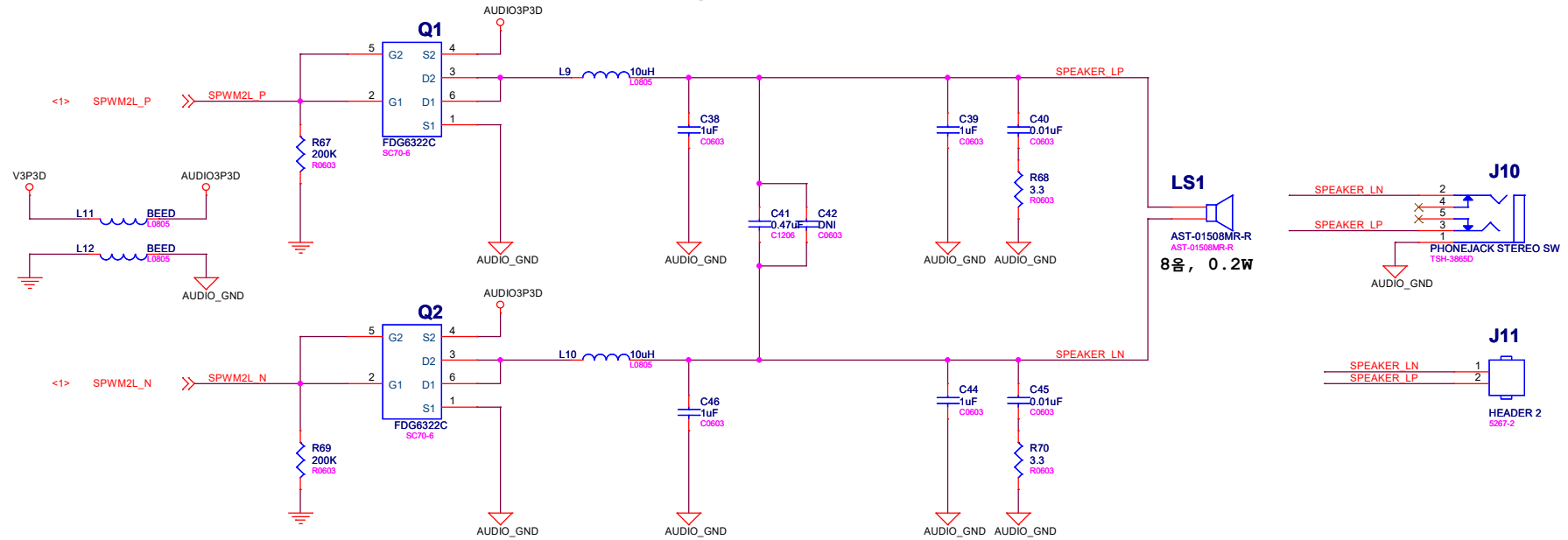
Condition	Red LED	Yellow LED	Green LED
ON solid			Connected over TCP
Fast blink	Not Associated	Rx/Tx data transfer	No IP address
Slow blink			IP address OK
OFF	Associated		



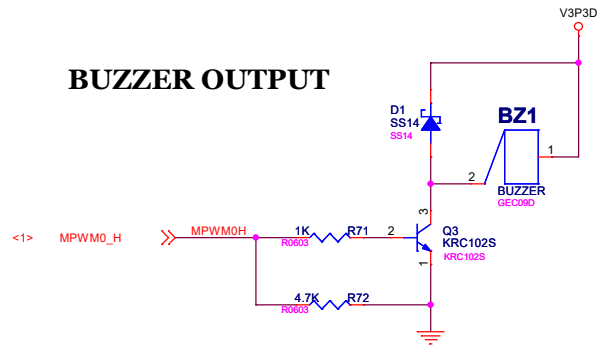
## UART/RS232

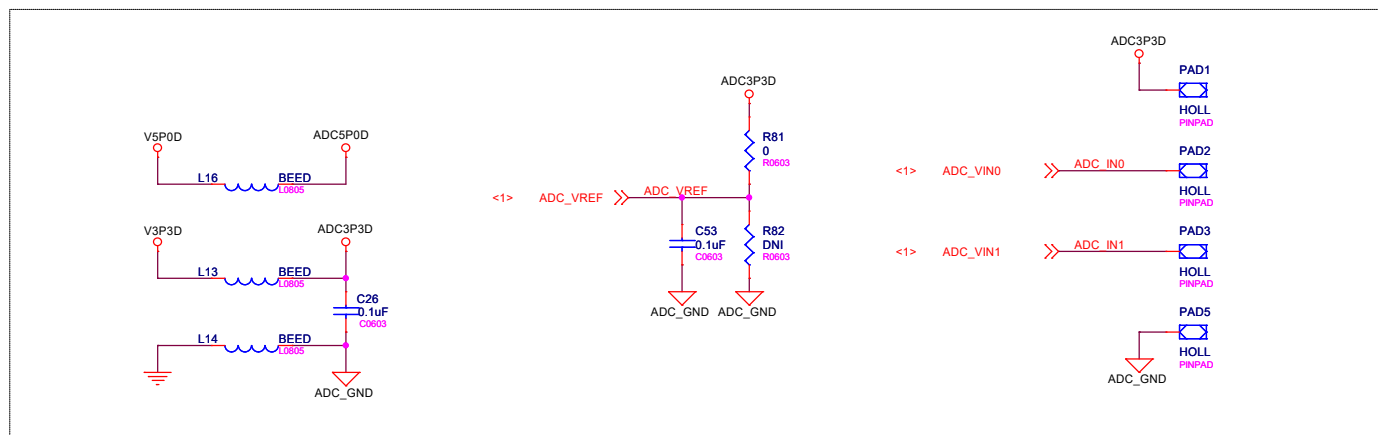
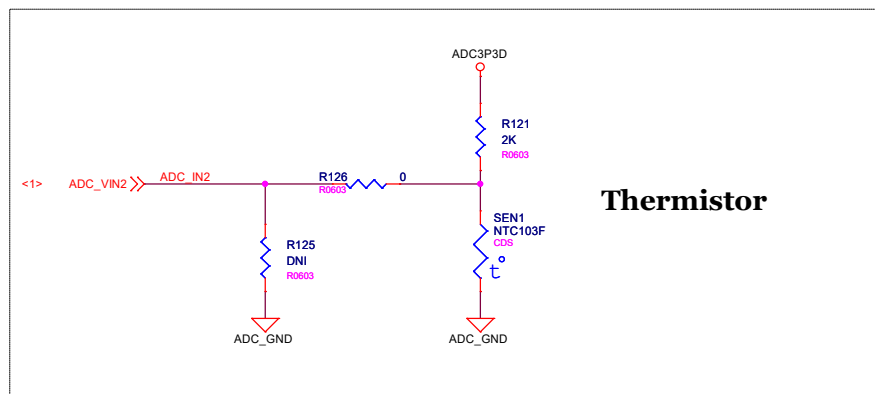
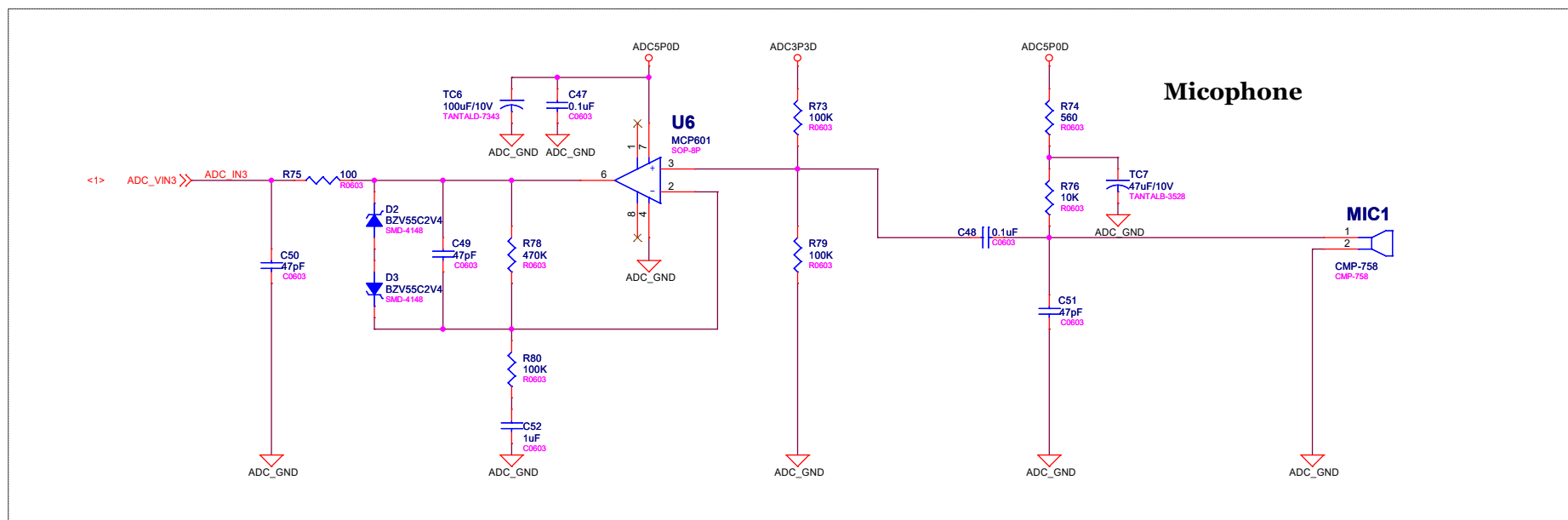


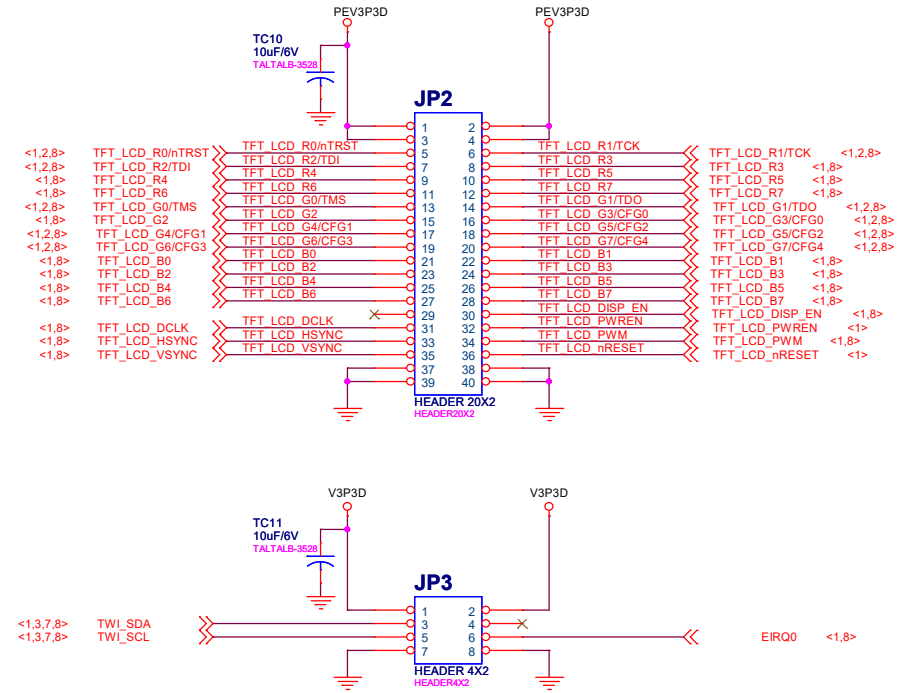
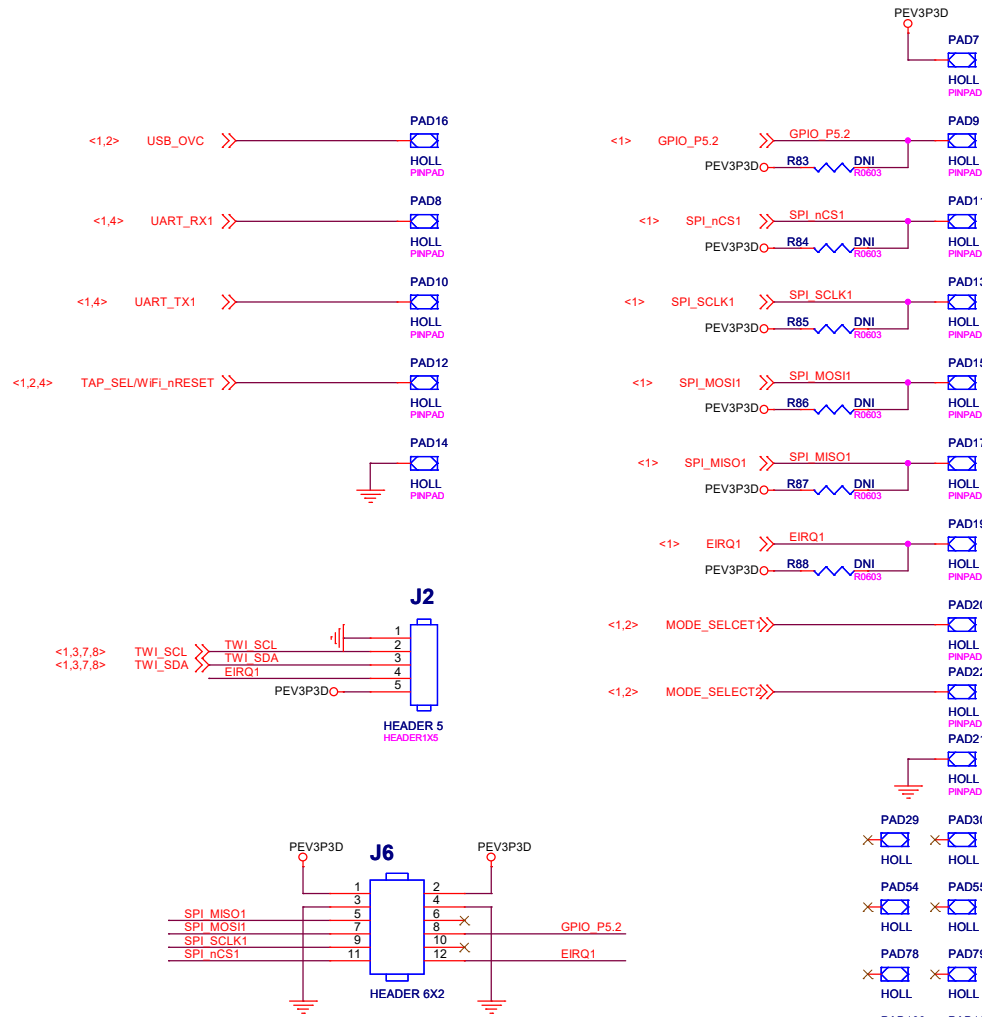
## Digital Audio



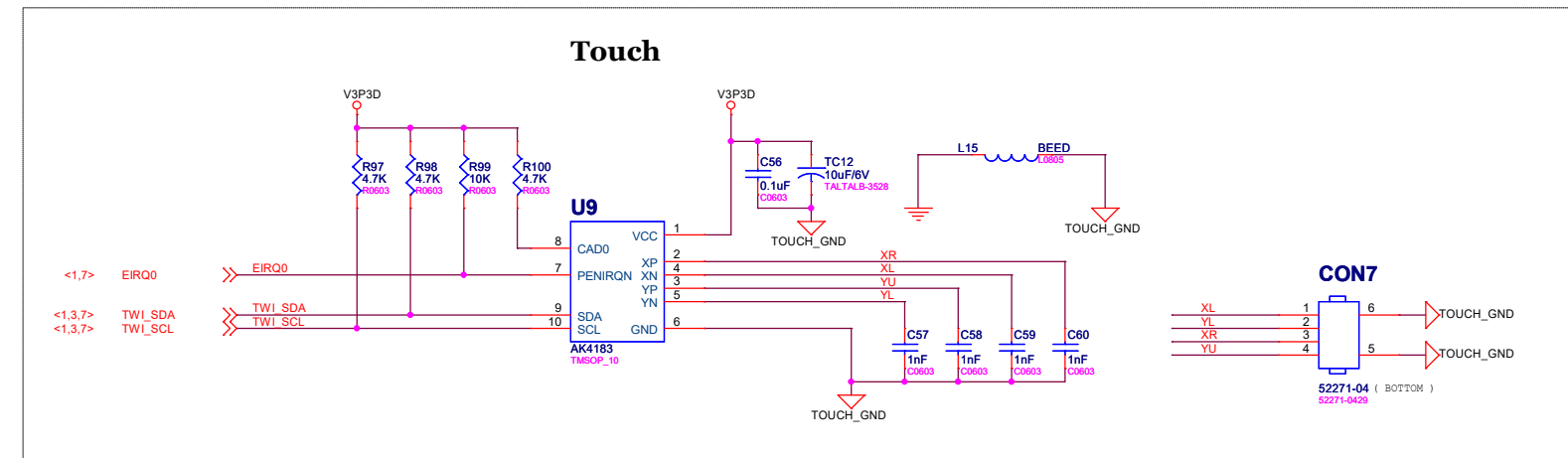
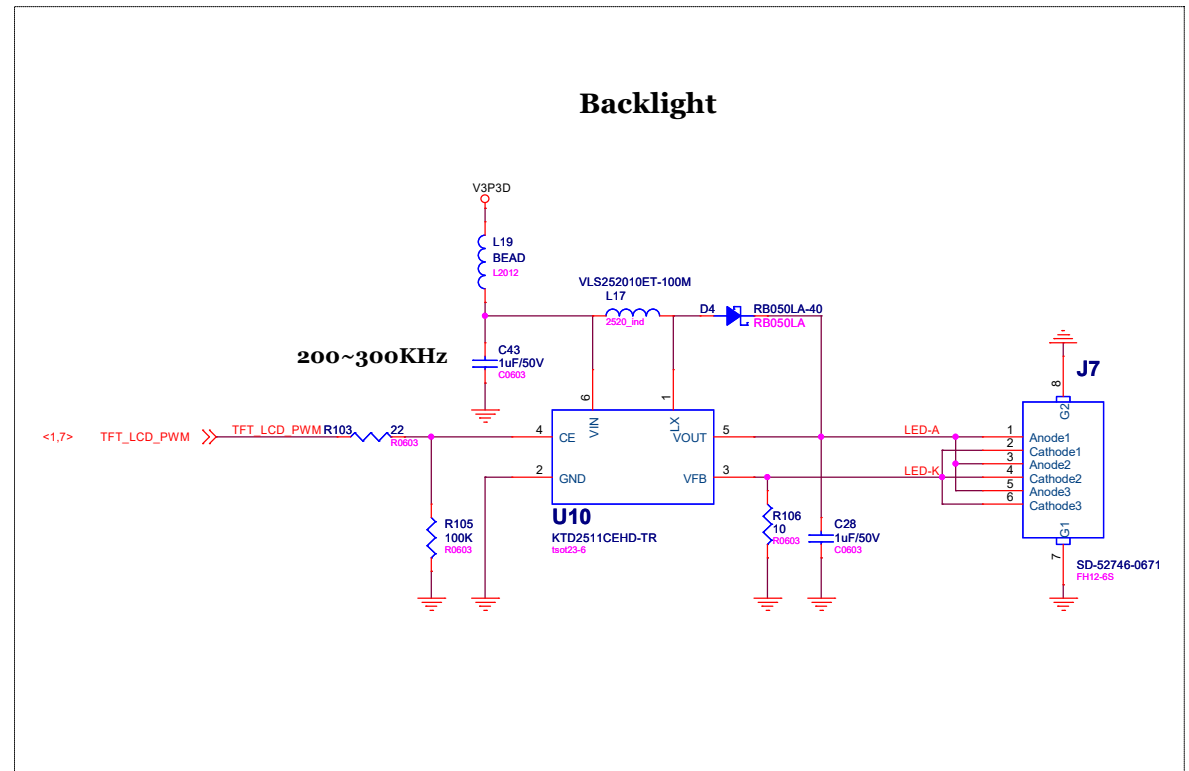
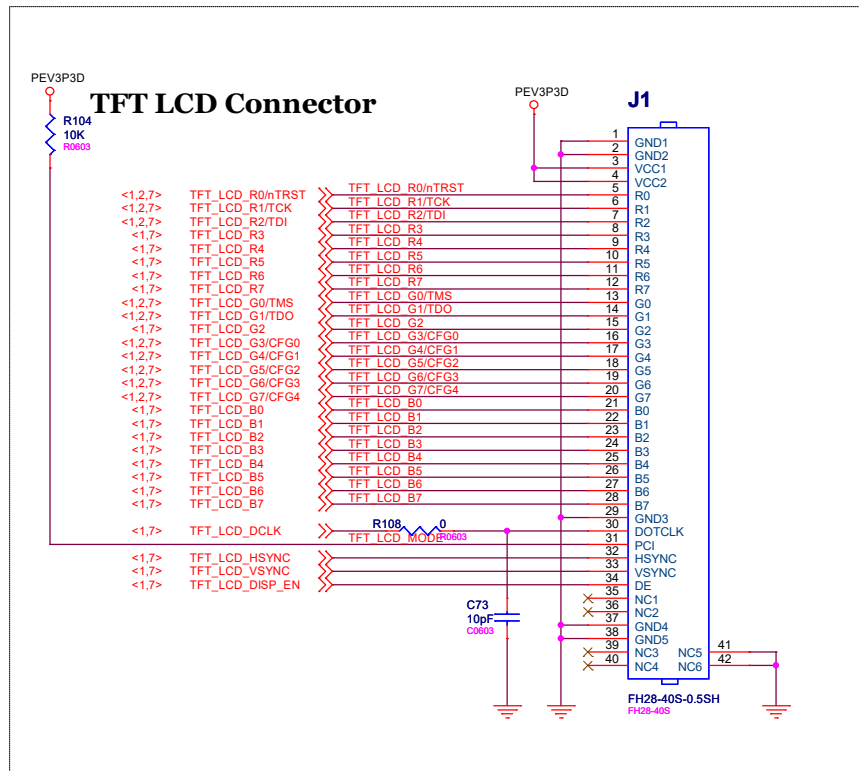
## BUZZER OUTPUT



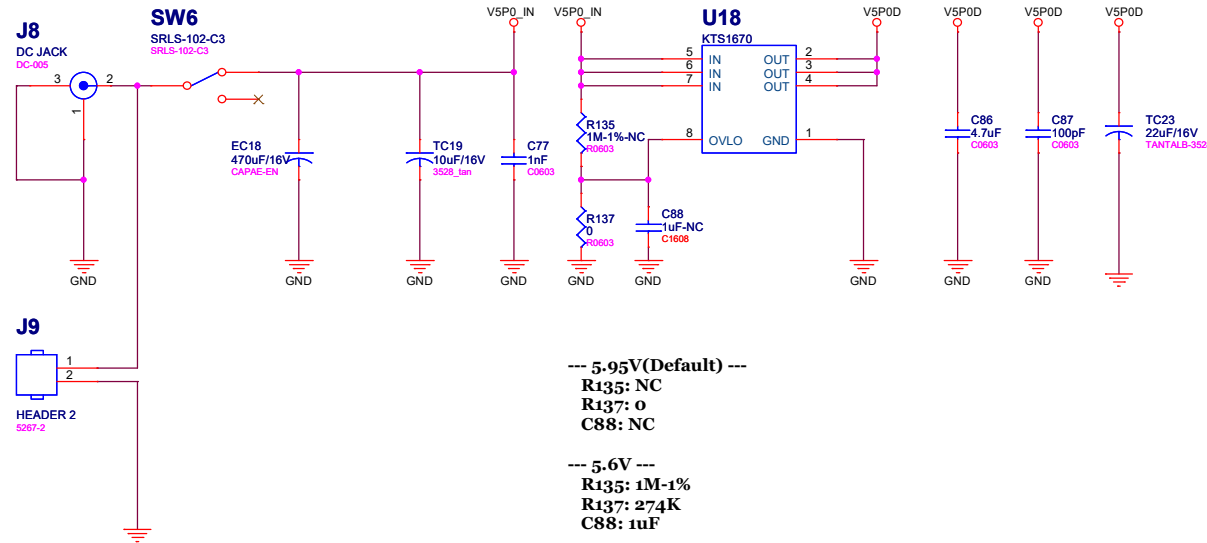




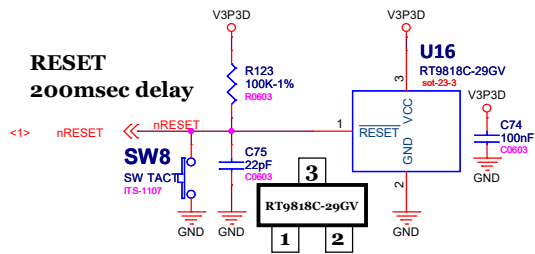
PAD29	PAD30	PAD31	PAD32	PAD33	PAD34	PAD35	PAD36	PAD37	PAD38	PAD39	PAD40	PAD41	PAD42	PAD43	PAD44	PAD45	PAD46	PAD47
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL
PAD54	PAD55	PAD56	PAD57	PAD58	PAD59	PAD60	PAD61	PAD62	PAD63	PAD64	PAD65	PAD66	PAD67	PAD68	PAD69	PAD70	PAD71	PAD72
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL
PAD78	PAD79	PAD80	PAD81	PAD82	PAD83	PAD84	PAD85	PAD86	PAD87	PAD88	PAD89	PAD90	PAD91	PAD92	PAD93	PAD94	PAD95	PAD96
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL
PAD103	PAD104	PAD105	PAD106	PAD107	PAD108	PAD109	PAD110	PAD111	PAD112	PAD113	PAD114	PAD115	PAD116	PAD117	PAD118	PAD119	PAD120	PAD121
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL
PAD128	PAD129	PAD130	PAD131	PAD132	PAD133	PAD134	PAD135	PAD136	PAD137	PAD138	PAD139	PAD140	PAD141	PAD142	PAD143	PAD144	PAD145	PAD146
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL
PAD153	PAD154	PAD155	PAD156	PAD157	PAD158	PAD159												
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL												
PAD166	PAD167	PAD168	PAD169	PAD170	PAD171	PAD172												
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL												
PAD179	PAD180	PAD181	PAD182	PAD183	PAD184	PAD185												
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL												
PAD191	PAD192	PAD193	PAD194	PAD195	PAD196	PAD197												
HOLL	HOLL	HOLL	HOLL	HOLL	HOLL	HOLL												
PAD203	PAD204	PAD205	PAD206	PAD207														
HOLL	HOLL	HOLL	HOLL	HOLL														



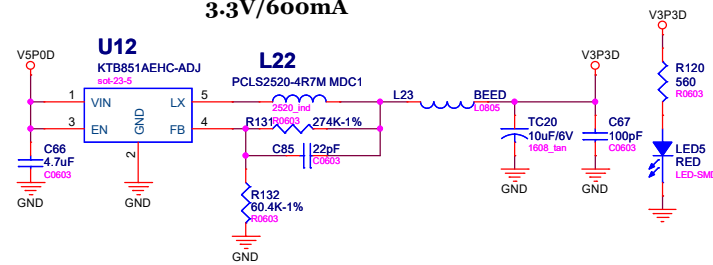
## INPUT 5V



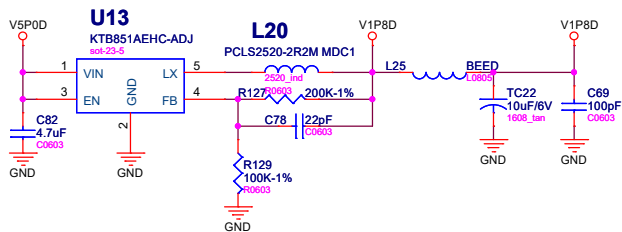
## RESET 200msec delay



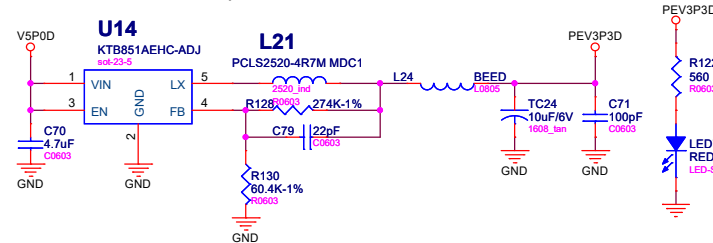
## 3.3V/600mA



## 1.8V/600mA



## 3.3V/600mA



# adStar-D STK ( adStar starter kit )

Version	Date	Description
Ver : 1.0	Nov 11, 2011	
Ver : 1.1	Nov 24, 2011	First release version
Ver : 1.2	Jan 05, 2012	adSar LDO_VBG ( C19 : 1uF -> 2.2nF 으로 변경)
Ver : 1.3	Jan 16, 2012	JP1 PIN6, PIN10 -> Ground
Ver : 1.4	Feb 21, 2012	외부 LDO 사용으로 변경.( R5 NC 처리, R6 0옴저항 삽입 )
Ver : 1.5	Feb 27, 2012	USB HOST BLOCK( TBD : To be determined )
Ver : 2.0	Jun 07, 2012	MODE SWITCH : adStar Pin 30,31 -> Pin 28,29 SPWM Output : adStar Pin 28,29 -> Pin 30,31
Ver : 3.0	April 03, 2013	Back-Light Drive IC : BD6066EKN -> R1204N313A-TR-FE 교체 J2 추가 : TOUCH IC AD1008 모듈용 J6 추가 : SPI INTERFACE
Ver : 4.0	July 13, 2018	***** 필수 수정 사항 ***** NAND Boot Mode: only 4bit ECC Mode Reset IC add ("RT9818C-29GV") 1. "Interface select" delete 2. OVP(Over Voltage Protection) add ("KTS1670") 3. 1.8V, 3.3V LDO Change (LM1118 -> "KTB851AEHC-ADJ") 4. LCD Backlight Change(R1204N313D-FE->"KTD2511CEHD-TR")
Ver : 4.1	Nov 19, 2018	JP1 14Pin->10Pin으로 변경