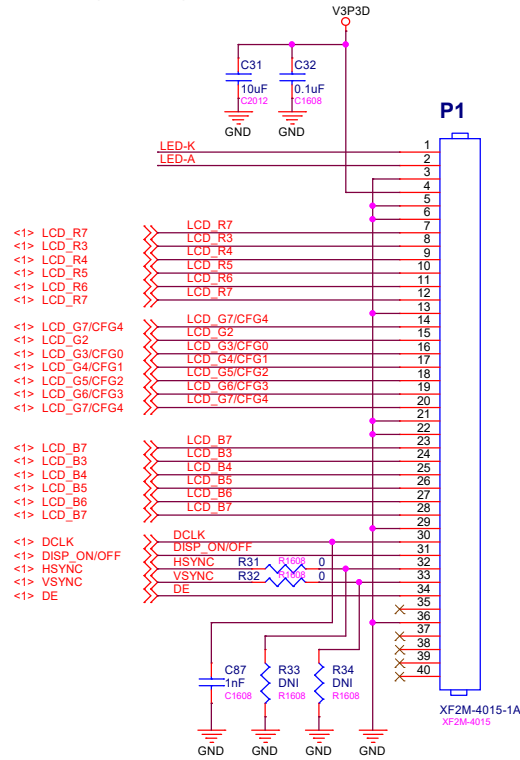
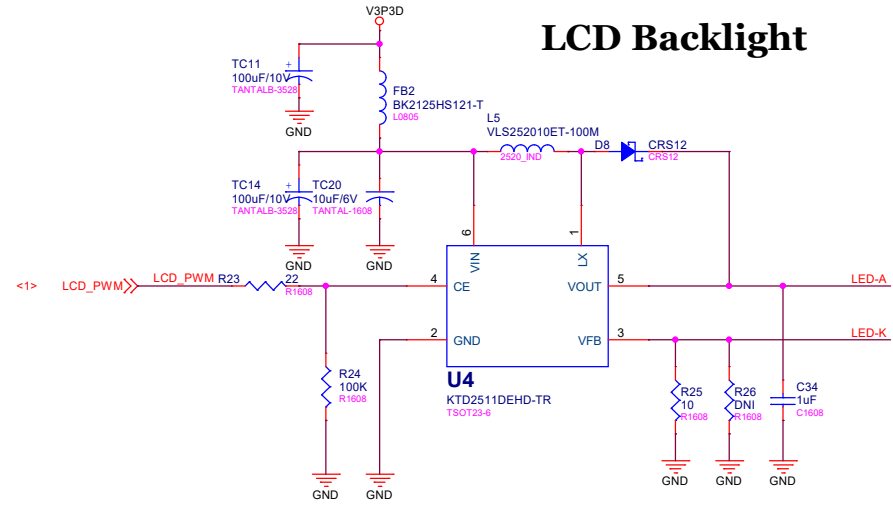


# 4.3" TFT LCD 5:6:5 INTERFACE

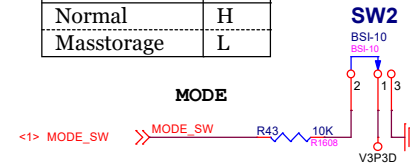


## LCD Backlight



## Mode Selection

BOOT MODE	
Normal	H
Masstorage	L

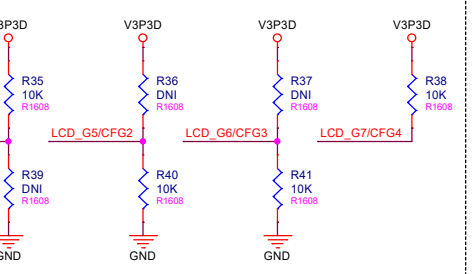
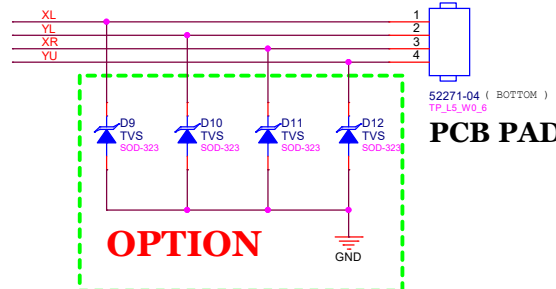
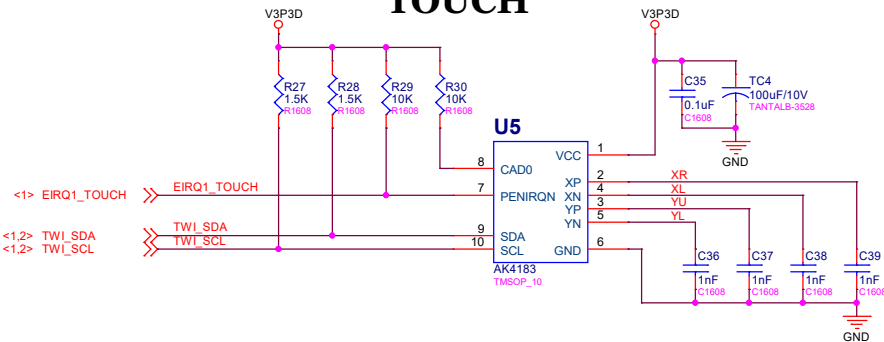


## Power On Config Mode Selection

Power On Config MODE	CFG[0..4]				
	0	1	2	3	4
JTAG DEBUG MODE	L	H	X	X	H
Serial Flash Boot Mode	H	H	L	L	H
NAND Boot Mode 4bit ECC	H	L	H	L	H



## TOUCH



**Power : 5V Input**

The schematic diagram illustrates the power input section for the 5V supply. It features a voltage regulator (U9, KTS1670) and various passive components. The input is connected to the regulator's IN pins (5 and 6). The output (pin 7) provides the 5V supply to the V5P0D pin. The schematic also shows the connection of the 5V input to the SD5268-06A and SD5268-6A components. The schematic is labeled 'Power : 5V Input'.

**Component Values:**

- R133: NC
- R135: 0
- C86: 1nF
- C88: 1uF
- C40: 0.1uF
- TC2: 100uF
- TC13: 100uF
- D15: TVS
- C46: 8.2pF
- D16: TVS
- C47: 8.2pF

**Options:**

- 5.95V(Default) ---
- 5.6V ---
- OPTION

**Labels:**

- SD5268-06A
- SD5268-6A
- V5P0D
- USER\_RX1
- USER\_TX1

**RESET**  
200ms delay

<1> nRESET << nRESET

SW3 75-110T

SW3 75-110T

R53 100K-1% R1608

C75 22pF C1608

V3P3D

U8 RT9818C-29GV sot-23-3

RESET

1 VCC

2 GND

3

V3P3D

C58 100nF C1608

GND

RT9818C-29GV

1 2 3

**CON3**

V3P3D

**TVS : PDS5VoL1BA**

53014-04  
S3014-04

GND

D17 TVS  
SOD-323

C51 8.2pF  
C1608

D18 TVS  
SOD-323

C52 8.2pF  
C1608

**OPTION**

R46 R1608 100  
R47 R1608 100

DBUG\_RX0  
DBUG\_TX0

DBUG\_RX0 <1>  
DBUG\_TX0 <1>

**1.8V/600mA**

**U7**  
KTB851AEHC-ADJ  
100-23-9

**L20**  
PCLS2520-2R2M MDC1

V3P3D

FB5

BK2125HS121-T

TC21  
10uF/16V  
TANTAL-1608

TC7  
100uF/10V  
TANTALB-3528

C49  
1uF  
C1608

1

3

2

VIN

LX

EN

GND

FB

5

4

R129  
100K-1%  
R1608

C78  
22pF  
C1608

V1P8D

L25

BEED

C50  
1uF  
C1608

TC8  
100uF/10V  
TANTALB-3528

CPU1P8D

Diagram illustrating the JP1 header connection. The header is a 7x2 pin connector. The connections are as follows:

- Pin 1: GND
- Pin 2: OTP\_6P3D V3P3D
- Pin 3: GND
- Pin 4: R51 (100 ohms) and R1608 (100 ohms)
- Pin 5: GND
- Pin 6: LCD\_G1/TDO
- Pin 7: GND
- Pin 8: LCD\_G1/TDO
- Pin 9: GND
- Pin 10: LCD\_G1/TDO
- Pin 11: GND
- Pin 12: LCD\_G1/TDO
- Pin 13: GND
- Pin 14: LCD\_G1/TDO

The signals are labeled as <1,3> LCD\_R2/TDI, <1,3> LCD\_G0/TMS, <1,3> LCD\_R0/nTRST, <1,3> LCD\_R1/TCK, and <1,3> LCD\_G1/TDO.

## adStar-D LCM

Version	Date	Description
Ver : 3.0	July 13, 2018	OVP(Over Voltage Protection) add ("KTS1670") 1.8V, 3.3V LDO Change (LM1118 -> "KTB851AEHC-ADJ") Reset IC add ("RT9818C-29GV") LCD Backlight Change(R1204N313D-FE->"KTD2511DEHD-TR")

 <b>adc</b> advanced digital chips inc.	<div data-bbox="1892 1364 2132 1382">(주)에이디칩스</div> <div data-bbox="1892 1388 2132 1398">14056 경기도 안양시 동안구 학회로 282, A동 22층 (관왕동, 금강원타워를 IT타워위) <a href="http://www.adc.co.kr">http://www.adc.co.kr</a></div>	
<div data-bbox="1722 1404 2186 1414">Title</div> <div data-bbox="1722 1414 2029 1445"> <b>adStar-D LCM</b> </div> <div data-bbox="2029 1414 2186 1445">           Designed by eunha.Kim  <a href="mailto:eunha@adc.co.kr">eunha@adc.co.kr</a> </div>		
Size <b>A3</b>	<div data-bbox="1771 1447 2134 1457">Page Subtitle</div> <div data-bbox="1771 1457 2134 1466"> <b>History</b> </div>	Ver. <b>3.0</b>
Date : Tuesday, July 24, 2018		Page 5 of 5