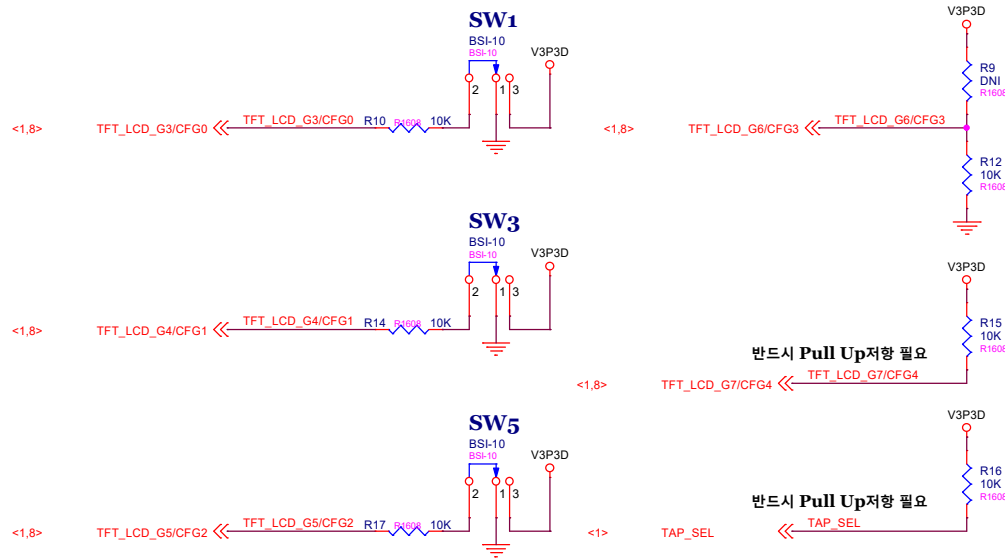




Boot Selection

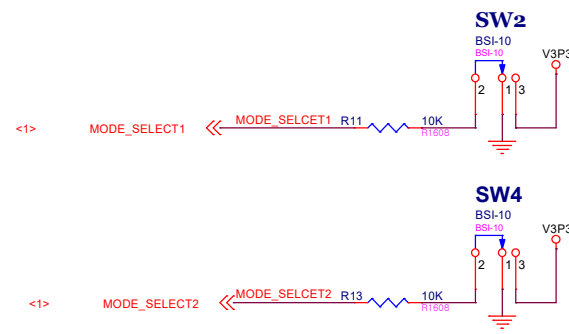


Boot Mode Selection

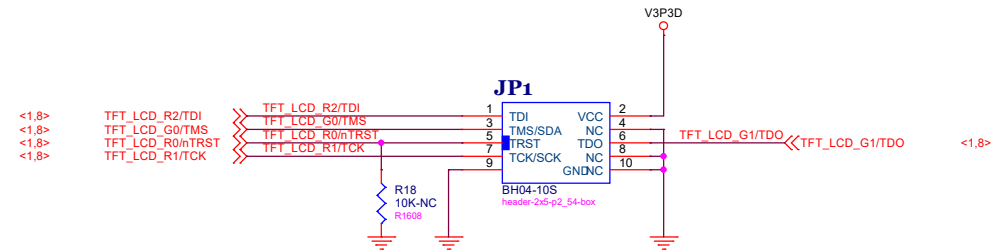
BOOT MODE	CFG[0..4]				
	SW1	SW3	SW5	R12	R15
JTAG DEBUG MODE	L	X	X	X	H
Serial Flash Boot Mode	H	H	L	L	H
NAND Boot Mode (4bit ECC)	H	L	H	L	H

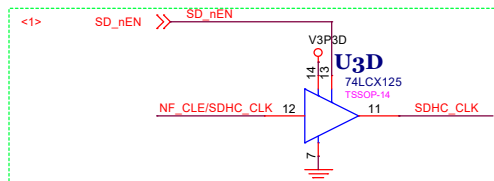
User Mode Selection

USER MODE	SW2	SW4
Execute	L	L
Mass Storage	H	L
USB Communication	L	H
Execute FAT	H	H



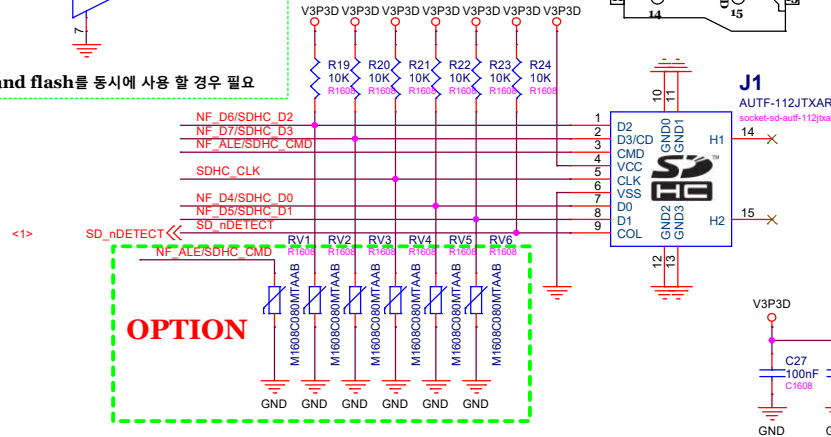
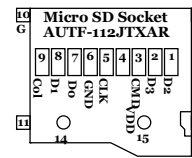
JTAG Connector





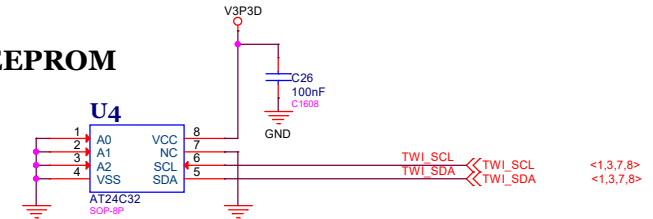
U3은 SD Card와 nand flash를 동시에 사용 할 경우 필요

micro SD Card

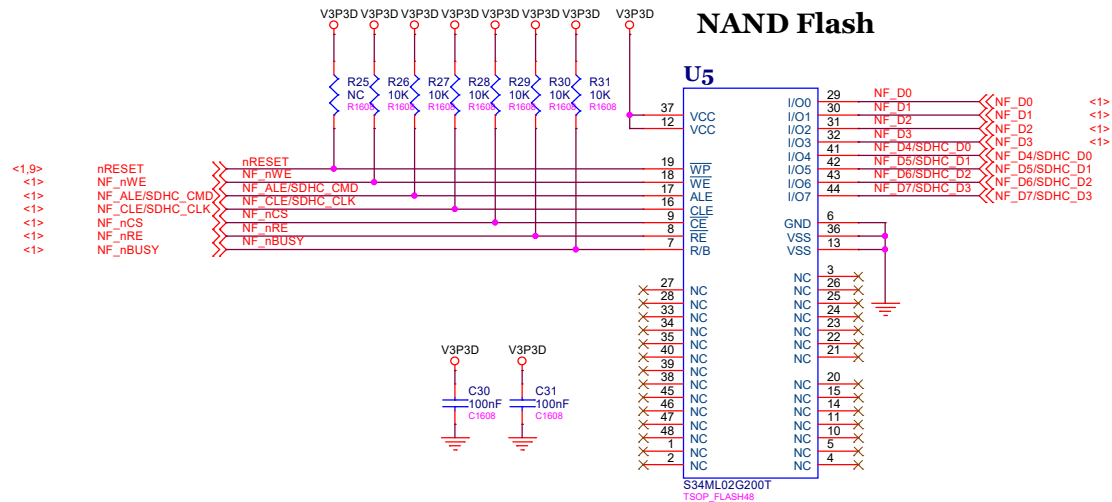


OPTION

EEPROM

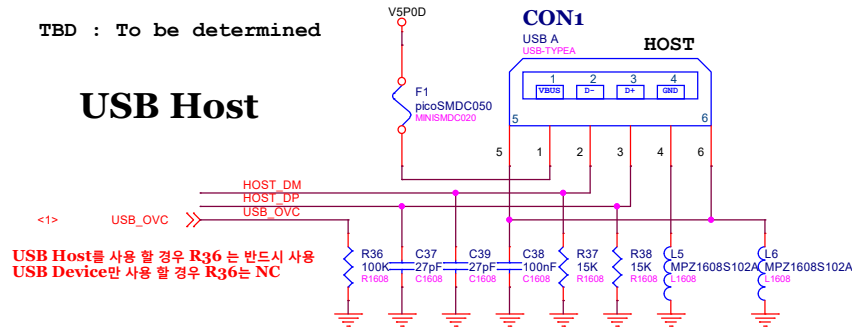


NAND Flash

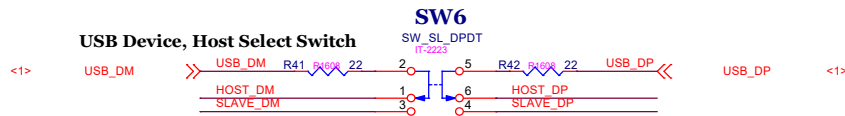


TBD : To be determined

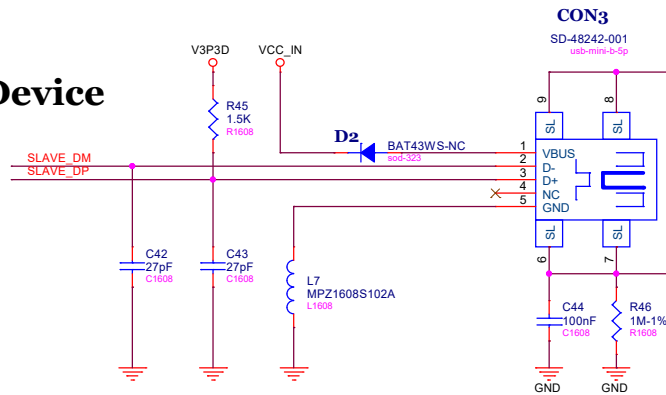
USB Host



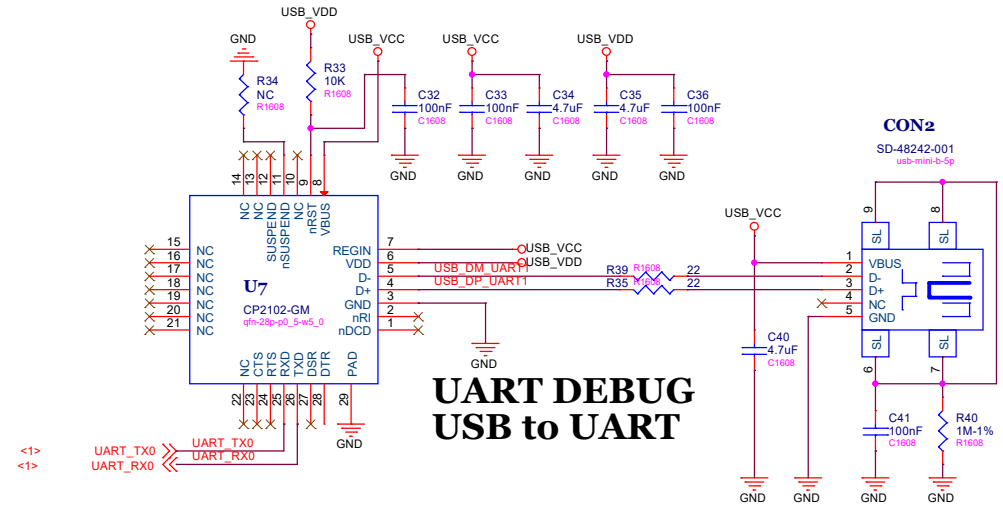
USB Device, Host Select Switch



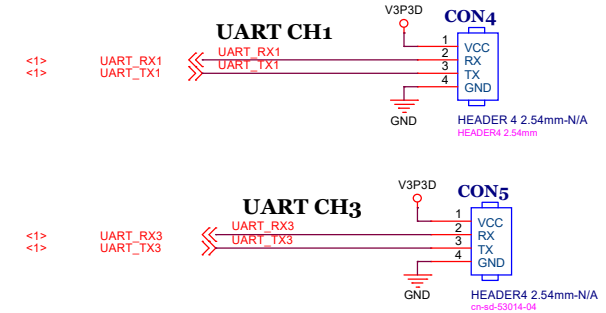
USB Device

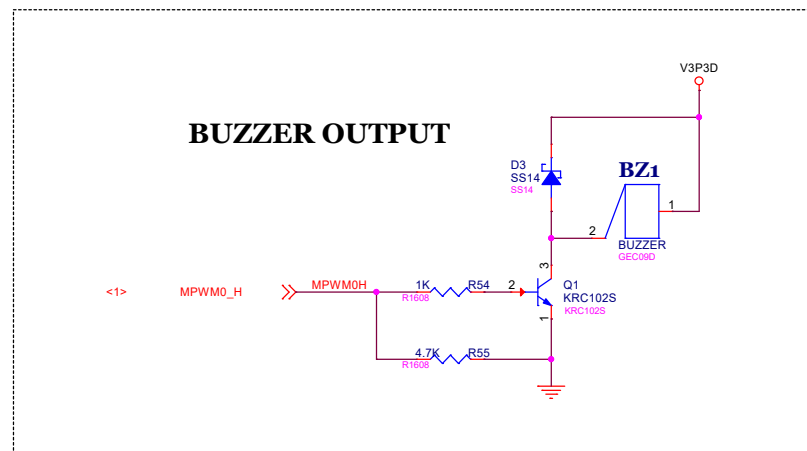
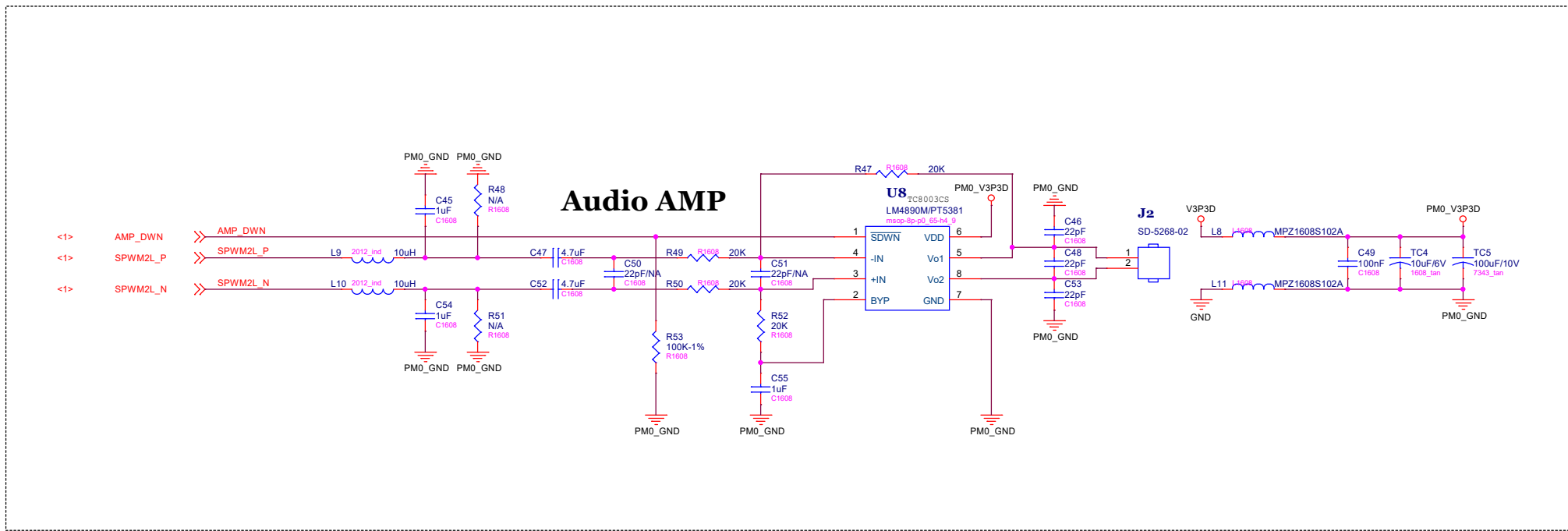


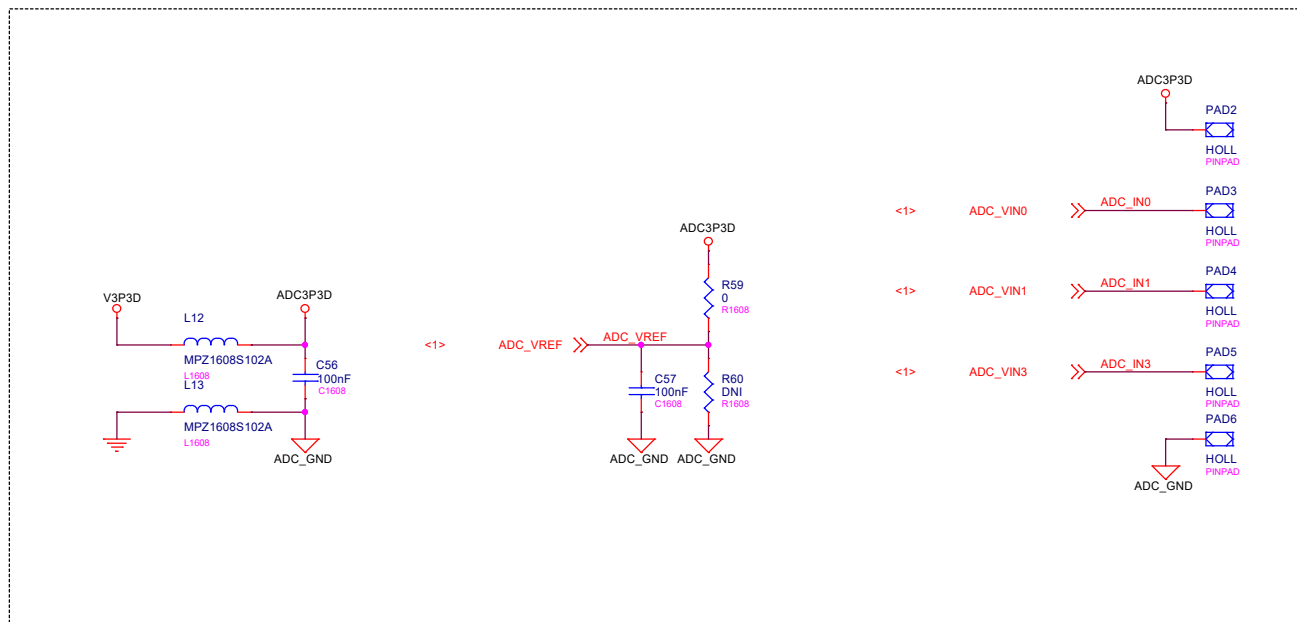
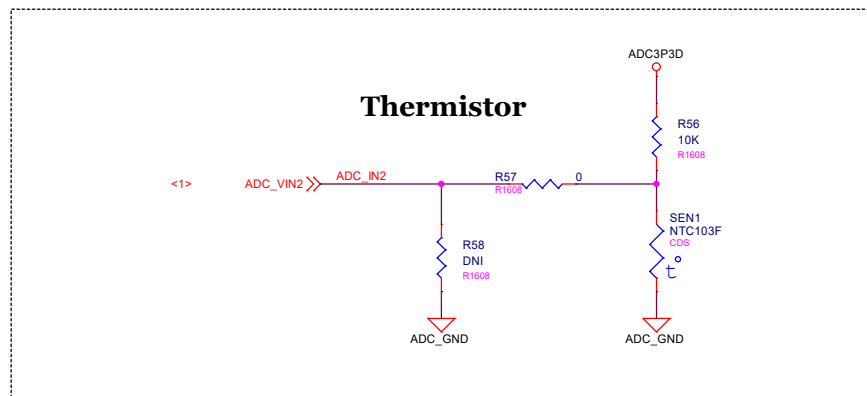
UART DEBUG USB to UART

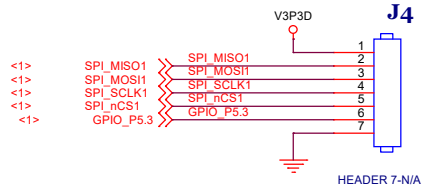
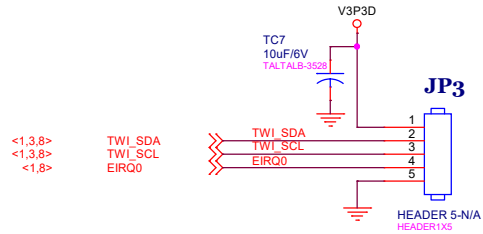


UART



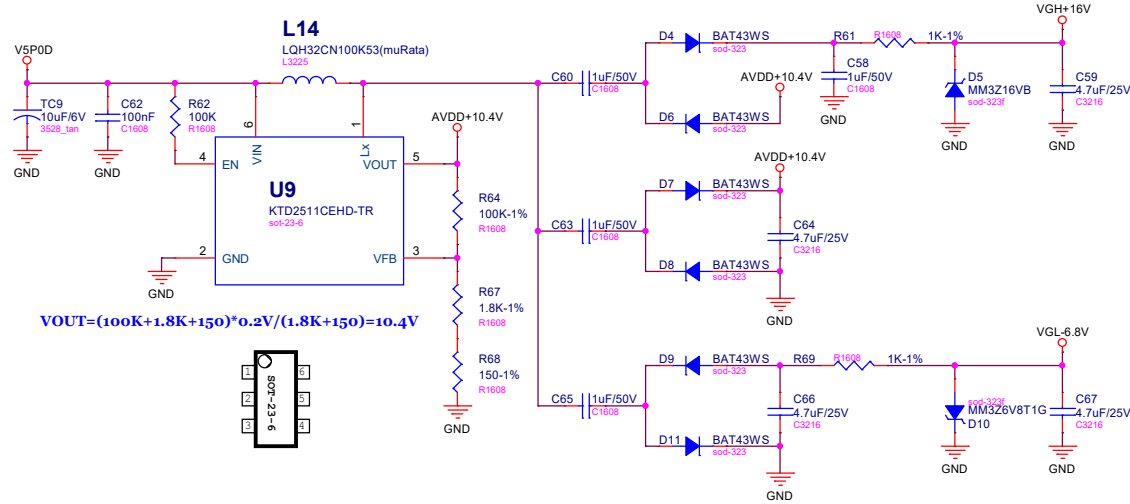






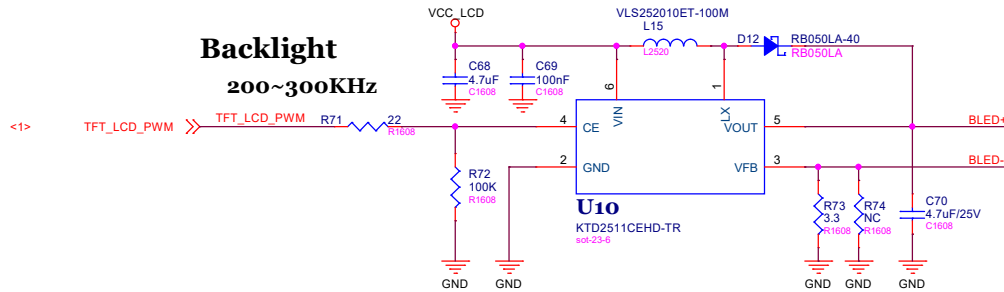
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PAD28	PAD29	PAD30	PAD31	PAD32	PAD33	PAD34	PAD35	PAD36	PAD37	PAD38	PAD39	PAD40	PAD41	PAD42	PAD43	PAD44	PAD45	PAD46
PAD47	PAD48	PAD49	PAD50	PAD51	PAD52	PAD53	PAD54	PAD55	PAD56	PAD57	PAD58	PAD59	PAD60	PAD61	PAD62	PAD63	PAD64	PAD65
PAD66	PAD67	PAD68	PAD69	PAD70	PAD71	PAD72	PAD73	PAD74	PAD75	PAD76	PAD77	PAD78	PAD79	PAD80	PAD81	PAD82	PAD83	PAD84
PAD85	PAD86	PAD87	PAD88	PAD89	PAD90	PAD91	PAD92	PAD93	PAD94	PAD95	PAD96	PAD97	PAD98	PAD99	PAD100	PAD101	PAD102	PAD103
PAD104	PAD105	PAD106	PAD107	PAD108	PAD109	PAD110												
PAD111	PAD112	PAD113	PAD114	PAD115	PAD116	PAD117												
PAD118	PAD119	PAD120	PAD121	PAD122	PAD123	PAD124												
PAD125	PAD126	PAD127	PAD128	PAD129	PAD130	PAD131												
PAD132	PAD133	PAD134	PAD135	PAD136														

LCD Gate Power

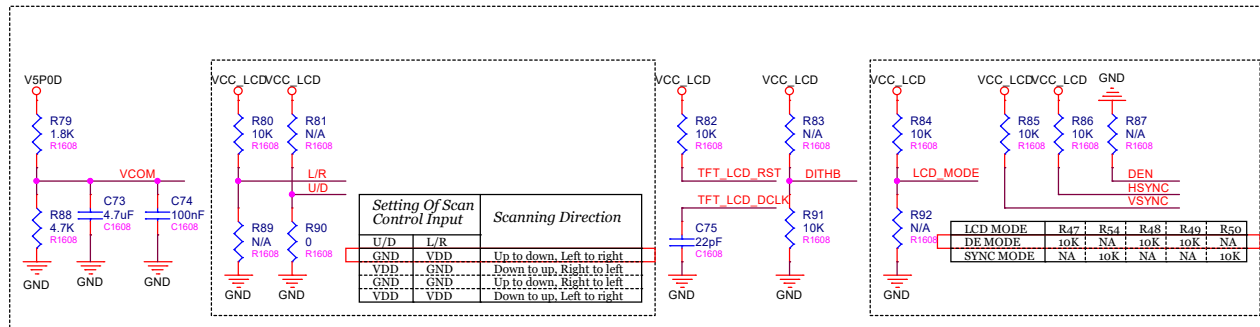
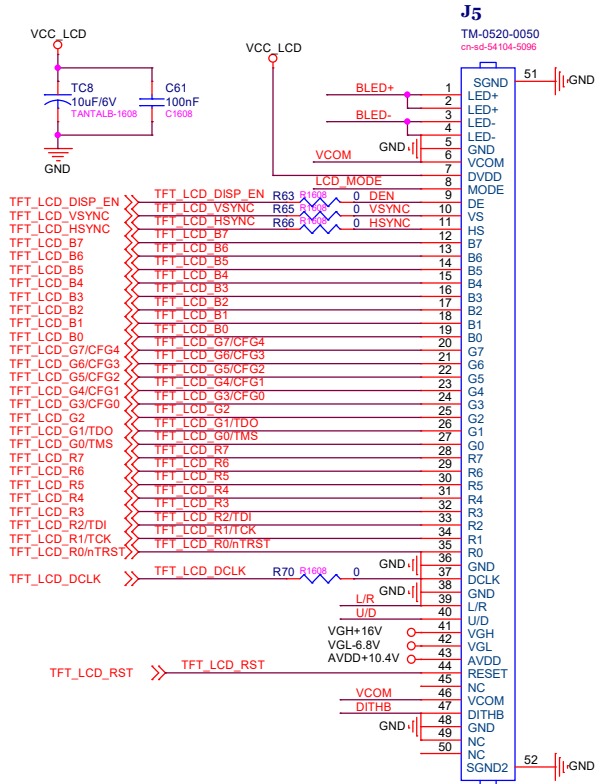
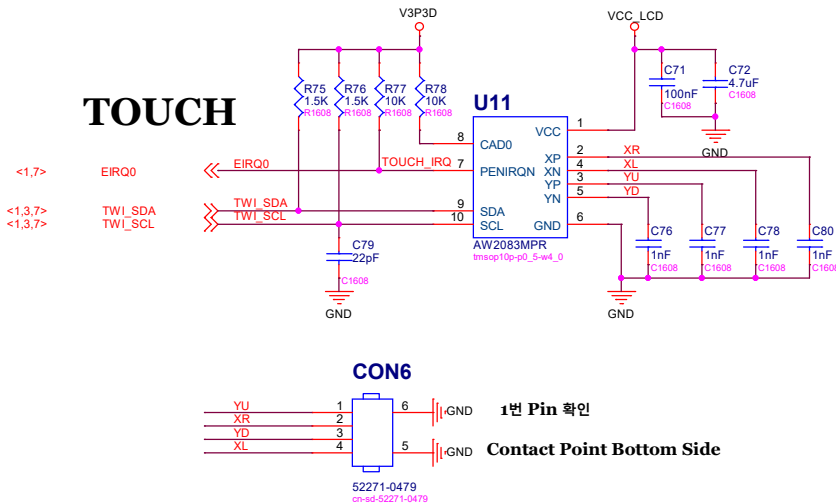


Backlight

200~300KHz



TOUCH



advanced digital chips inc.

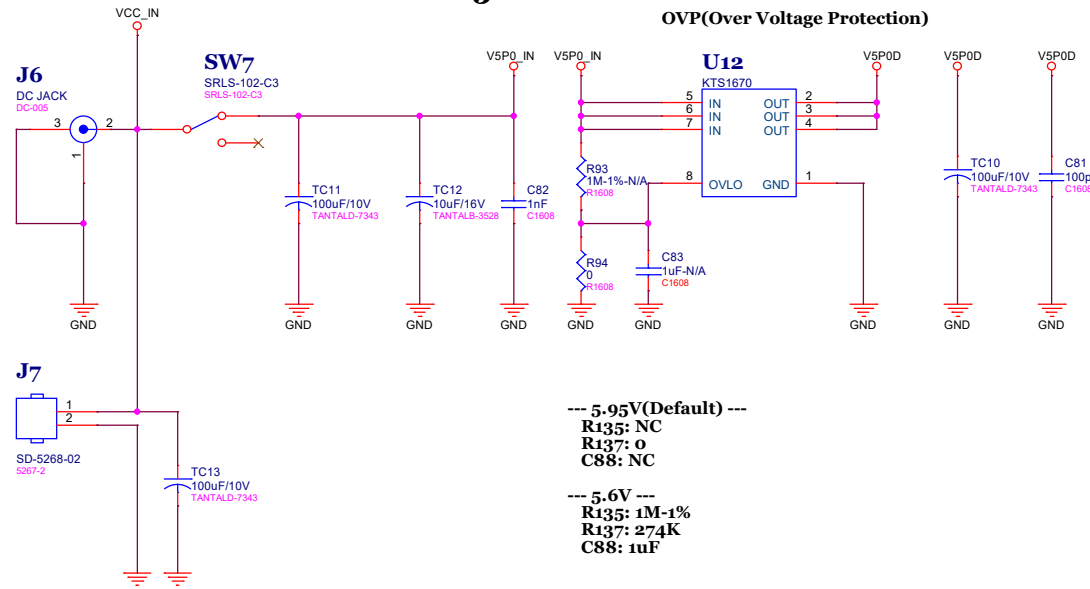
(주)에이디칩스

14056 경기도 안양시 동안구 학회로 282, A동 22층
(관양동, 금강펜타리움 IT타워)
<http://www.adc.co.kr>

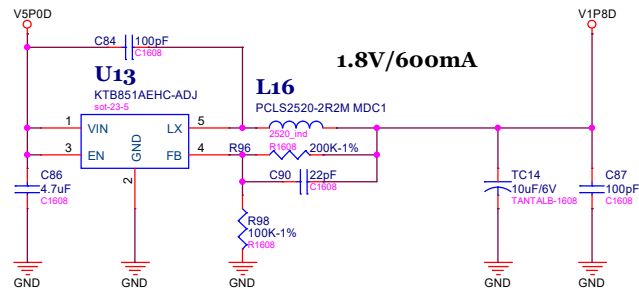
Title		Designed by eunha.Kim	
adStar-D STK		eunha@adc.co.kr	
Size	Page Subtitle	Ver.	
A3	TFT LCD(T070P40PS01RNJ-W)	5.0	
Date	Wednesday, October 21, 2020	Page	8 of 10

INPUT 5V

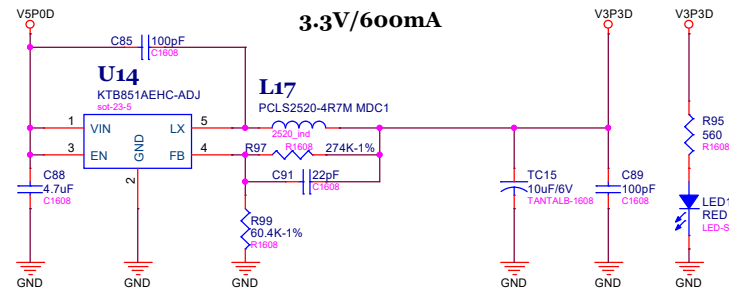
OVP(Over Voltage Protection)



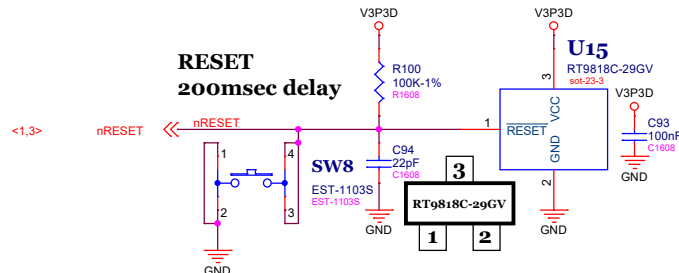
1.8V/600mA



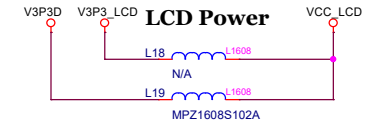
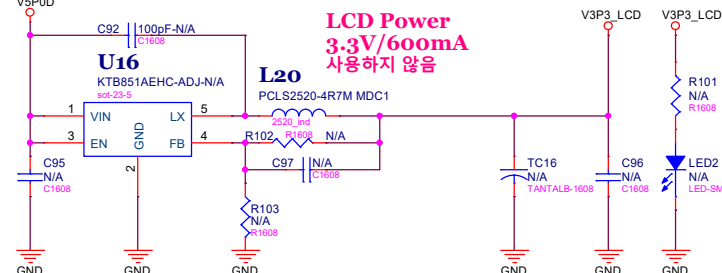
3.3V/600mA



RESET 200msec delay



LCD Power 3.3V/600mA 사용하지 않음



 advanced digital chips inc.		(주)에이디칩스 14056 경기도 안양시 동안구 학회로 282, A동 22층 (관양동, 금강벤처리움 IT타워) http://www.adc.co.kr	
Title adStar-D STK			
Size A3	Page Subtitle Power & Reset	Designed by eunha. eunha@adc.co.kr	
Date Thursday, December 22, 2022	Page 9 of 10	Ver. 5.0	

adStar-D STK (adStar starter kit)

Version	Date	Description
Ver : 1.0	Nov 11, 2011	
Ver : 1.1	Nov 24, 2011	First release version
Ver : 1.2	Jan 05, 2012	adSar LDO_VBG (C19 : 1uF -> 2.2nF 으로 변경)
Ver : 1.3	Jan 16, 2012	JP1 PIN6, PIN10 -> Ground
Ver : 1.4	Feb 21, 2012	외부 LDO 사용으로 변경.(R5 NC 처리, R6 0옴저항 삽입)
Ver : 1.5	Feb 27, 2012	USB HOST BLOCK(TBD : To be determined)
Ver : 2.0	Jun 07, 2012	MODE SWITCH : adStar Pin 30,31 -> Pin 28,29 SPWM Output : adStar Pin 28,29 -> Pin 30,31
Ver : 3.0	April 03, 2013	Back-Light Drive IC : BD6066EKN -> R1204N313A-TR-FE 교체 J2 추가 : TOUCH IC AD1008 모듈용 J6 추가 : SPI INTERFACE
Ver : 4.0	July 13, 2018	***** 필수 수정 사항 ***** NAND Boot Mode: only 4bit ECC Mode Reset IC add ("RT9818C-29GV") 1. "Interface select" delete 2. OVP(Over Voltage Protection) add ("KTS1670") 3. 1.8V, 3.3V LDO Change (LM1118 -> "KTB851AEHC-ADJ") 4. LCD Backlight Change(R1204N313D-FE->"KTD2511CEHD-TR")
Ver : 4.1	Nov 19, 2018	JP1 14Pin->10Pin으로 변경